

Notice of References Cited

Application/Control No.

09/477,790

Applicant(s)/Patent Under
Reexamination
BARRETT, GEOFF

Examiner

Herng-der Day

Art Unit

2123

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,491,639	02-1996	Filkorn, Thomas	716/5
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Pixley et al., "Exact Calculation of Synchronizing Sequences Based on Binary Decision Diagrams", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, August 1994, pages 1024-1034.
	V	Cabodi et al., "Symbolic Exploration of Large Circuits with Enhanced Forward / Backward Traversals", Proceedings of the Conference on European Design Automation Conference, 1994, pages 22-27.
	W	Iwashita et al., "CTL Model Checking Based on Forward State Traversal", Proceedings of the 1996 IEEE/ACM International Conference on Computer-Aided Design, November 1996, pages 82-87.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.